

MEMORY MAPPING APPARATUS, SYSTEMS, AND METHODS

Abstract of the Disclosure

5 An apparatus and a system, as well as a method and article, may operate to
map, by an operating system, a range of virtual addresses to a range of physical
addresses, wherein a subset of the range of virtual addresses is identity-mapped to a
subset of the range of physical addresses. This range of addresses can then be used
by heavy data transfer applications in conjunction with a DMA engine to transfer
10 data between memories, or between memory and input/output devices.

"Express Mail" mailing label number: EV298565755US

Date of Deposit: November 17, 2003

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